

# **EuroHPC Leonardo Consortium**

### EWHPC 2020 - Mirko Cestari









REPUBLIKA SLOVENIJA REPUBLIC OF SLOVENIA Ministrstvo za izobraževanje, znanost in šport Ministry of education, science and sport Masarykova cesta 16, SI - 1000 Ljubljana

# **Joint effort process**

### EuroHPC JU

• conducting the Procurement process

### CINECA

- 10 staff members
- cover technical aspects of the procurement

### INFN

- 2 staff members
- provide consultancy and support for facility integration







2009	2012	2016	2019/2020	2021	2023-2025	2025-2027
IBM SP6 Power6	Fermi IBM BGQ PowerA2	Marconi Lenovo Xeon+KNL	Marconi PPI4HPC ICEI - PPIHBP	pre-Exascale with EuroHPC contribution	post-Marconi Exascale pilot technology (National Research Plan)	EuroHPC post Exascale/ National Full Exascale

## **Proposed System as EuroHPC HE**

System name	Leonardo					
Modules	3 (booster, general purpose, data centric)					
Number of computing nodes	5000 (3500+500+1000)					
Storage	Capacity: 150 PB, bandwidth: 1 TB/s					
HPL Targeted Performance (peak)	150-180 PFlops (210-250 PFlops); Top 3					
HPCG Targeted Performance	2.8-3.3 PFlops; Top 3					
I/O	≥ 150 PB					
Interconnection Bandwidth	≥ 200 Gb/s					
Estimated Power consumption (after PUE)	8-9 MW (8.8-9.9 MW)					

Use cases:

- 10x computing capability in a large set of key applications for science, industry and society (CoEs, HEP, Pharma, Oil&GAS), and keep the European leadership:
- gain sovereignty on strategic technologies for the European economic wealth, like Artificial Intelligence, Cybersecurity and Internet of Things;
- tackle relevant and urgent societal challenges.







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CINECA

### EuroHPC hosting @ Bologna Science Park CINECA



## **Procurement goal**



- HPC system
- 5 year maintenance and support
- Software stack and licenses

### **Consortium vision**

- Sustain the computing capability for the consortium and European researchers
- Build on top of **past experience** and results
- Allow for continuity in project development towards heterogenous systems

## Procurement goal cont.

### **Technical specifications**

- promote **competition**
- most of the mandatory requirements for the facility integration
- few mandatory requirements for the node design
  - Mostly for Data Centric and General Purpose nodes
  - $\circ$  > number of cores, NVM, DRAM
- Allow to propose the same design for Data Centric and General Purpose nodes
- Pretty free Booster node design
  - o Competitive dialogue as a guarantee to avoid too esoteric architecture/design
  - Can result in more aggressive offers
- I/O design
  - Requirements on capacity and aggregated performance

## **Booster Node comparison**

	Marconi-100	Leonardo
CPU	2 POWER9	> = 1
Cores	32 (16 per P9)	No hard requirement
Memory	256 GB	Enough to saturate channels
Memory Bandwidth	340 GB/s (2 x 8 x 21.25)	No hard requirement
CPUs : Accelerators	2:4	No hard requirement
Accelerators	4 Volta V100	No hard requirement
System Memory	320 GB	No hard requirement
Accelerator DP Flops	28 TF	No hard requirement
Accelerator Memory	64 GB HBM2	No hard requirement
Accelerator Memory Bandwidth	3.6 TB/s (900 GB/s x 4 GPUs)	No hard requirement
SSD Capacity	1.6 TB	No hard requirement

#### **Compute Partition**

Booster Module ? nodes ? Accelerator / node ?GB / node Data Centric Module 512 nodes, >= 64 core per node >=512GB / node 4TB SSD / node 3TB NVM / node

General Purpose Module 1024 nodes >= 48 core per node >=192GB / node 4TB SSD / node

#### Low Latency Interconnect 200Gb/s/link

#### 100Gb Ethernet Interconnect

Storage High IOPS, 5PByte, 1TB/s Storage High capacity Tier 100PByte, 500 GB/s

Front-end 16 Login nodes >= 32 cores, 256 GB, 10TB SSD 16 Viz nodes, 2 GPUS, >= 32 cores, 256 GB, 6TB

Firewall Link to GEANT & Cineca CINEC.



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Gateway

#### 100Gb Ethernet Interconnect

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### **Procurement Timeline**



## **Procurement Timeline**

- Technical specifications to enter the competitive dialogue released in January 2020
- Competitive dialogue dates:
  - + Session I, 13-14 February 2020 (4 hours each candidate)
  - ✦ Session II, 2-3 April 2020 (4 hours each candidate)
  - + Session III, 14-15 May 2020 (4 hours each candidate)
- □ Final RFP released on June 4, 2020
- □ BAFOs (best and final offers) expected June 29, 2020

Evaluation July 2020

□ Award Autumn 2020

- The most relevant criteria
  - 45/120 points (35%)
- 3 contributions

Performance of the system Combination of HPL and HPCG	<b>TCO Analysis</b> Based on benchmark suite	Benchmark Analysis assessment Completeness of results and projection methodology
40%	30%	30%

## **TCO Analysis**



### **TCO** analysis

- Built on top of PPI4HPC experience (<u>https://www.ppi4hpc.eu/</u>)
- sharing experience and lesson learned between partners (BSC, CINECA, CEA, JSC)
- Define a **common framework**
- Tested and proved

### Aim to define a "value for money" criteria

- The "Value" bases on the estimated (average) number of workloads that can be run on the entire lifetime of the system
- "Money" comes from a **TCO approximation**



## **TCO Analysis**



- Benchmark suite preparation took 4 months
  - Only 4 applications were taken into account
  - Well tested on multiple HPC architectures to avoid last minute surprise
  - Easy to manage for Procurer and Candidates
- All input sets, applications and instructions were publicly available through a git repository
  - Early release of small input to test code
  - We request Candidates to check the repository frequently
  - Complete set before 2nd meeting of the Dialogue Phase
- We provided
  - A Changelog to track repository updates
  - A Benchmark information document
     All rules for compilation and bechmark run
  - Questions and Answers document
    - •To track requests from Candidates
    - •All questions were anonymized and answered

B benchmark  Project ID: 482		🏠 Star 🛛 0
152 Commits 🏼 P 1 Branch 🖉 0 Tags 🖻 2.3 (	B Files 🛛 🔁 2.3 GB Storage	
master senchmark	History	Find file 📇 🗸 Clone 🗸
Aligning README and Changelo file Mirko Cestari authored 4 months ago		94e2fb59 🛱
README CHANGELOG I No license	All rights reserved Auto DevOps enabled	
Name	Last commit	Last update
MILC .	Fixed directory tree	5 months ago
PLUTO	Added changelog in the Pluto README	5 months ago
QuantumESPRESSO	Removed difference between MARCONI and GALILE	EO 5 months ago
SPECFEM3D	correction of typos	5 months ago
Changelog	Aligning README and Changelo file	4 months ago
Lot3-Benchmark_Information_v2.0.0.docx	Updated version of the Benchmark Information doc	um 5 months ago
Lot3-Benchmark_Information_v2.0.1.docx	correction of typos	5 months ago
A-Benchmarkprocedure-v2.1.pdf	Added clarification on SPECFEM3D bechmark	4 months ago
README IST	WWW.cineca.it	4 months and



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C2	1	7.5	0.215		64	7	6	1		64		Lk		150000		2700
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C1	42	1,2	1,018		16	40	5	1,018		16		C2		158000.50		5001.6
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C2	16	3,9	0,377		16	15	3	0,72		16		C3		1.00		0.22
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C1		83	1,492		16	54	0	2,6	1	16			Pc			
C2	76	2,7	2,33		16	61	7	2,8	1	16		C1		0,35		
C3	2	73	1,985		4	247	3	2	2	4		C2		0,67		
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## **Evaluation of the offers**



### CINECA technical team

- Worked on the **technical response template** 
  - $\circ~$  Offers a common way to provide the BAFO
  - Ease **comparison** between offers (tables)
  - Clearly state the **scoring principles**
  - All technical specification requirements must be mapped into scoring points
- Provided the Cost Performance Analysis Template
  - Ease evaluation process

Result: We received 3 very competitive offers. Each one was very worth it

## **Lessons learned**

- Enter the competitive dialogue stage with few hard requirements
  - Exploit the competitive dialogue process
  - $\circ$   $\,$  Try as much to be inclusive
  - $\circ$  Adjust to meet technology roadmap
  - Lower priority requirement if possible

- o Early release of Benchmark suite
- $\circ$  simplify as possible to ease evaluation
- Assessments of benchmark quality

### Leonardo



### Thank you

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